

**Applications are invited for Junior Research Fellow (JRF)/
Project Associate (PA)
at Indian Institute of Technology (IIT)-Mandi, MANDI-175005
(H.P.), India.**

Advertisement No: IITM/SKS/30/08/2018

Fellowship/Salary: The selected candidates will be appointed as Junior Research Fellow (JRF)/Project Associate for **Indian Space Research Organization (ISRO), Government of India**, sponsored project in the School of Computing and Electrical Engineering, IIT Mandi.

(The number of position may be vary as per requirement of the project)

ISRO sponsored project entitled “**Design and Fabrication of Interface ASIC for Vibratory Gyroscope Sensor Application**” at IIT Mandi, with fellowship/salary **Rs.20,000-25,000** per month **consolidated**. *(On subject to qualification and experience)*

Duration: The appointment will be made initially for a period of 1 Year, extension is possible further, based on the performance.

Additional information:

***** Deserving candidate may have the opportunity to join MS or Ph.D. at IIT Mandi as full-time/part time on the subject to fulfilling eligibility criteria and satisfactory performance as per Institute norms.**

Last Date of Application: 30/9/2018

ESSENTIAL EDUCATIONAL QUALIFICATION: Potential candidates should have B.Tech ECE/ EE/Instrumentation (Valid NET or GATE score preferable) or M.Sc. in Electronics Science (Valid NET or GATE score preferable) or equivalent degree with 60% marks or MS or M.Tech. in Electronics/VLSI/ECE (NET or GATE score is not desired for M.Tech candidates).

DESIRABLE QUALIFICATION:GATE/NET (JRF/LS)/equivalent with practical research experience (One or more years after M.Sc.) and the knowledge of **analog and digital circuit design** essential, Hands-on **experience on Matlab and VLSI tools such as Cadence or Synopsys** is important. The applicant must have experience of using Linux operating system preferably.

How to apply: *Eligible candidates should apply via email with full detail of their qualification on enclosed form at E-mail for applications:*
palit.sharma@gmail.com

Contact Address:**Mr. Kumar Palit**

Center for Design and Fabrication of Electronics Devices
(C4DFED)

A4 Building (IIT Mandi Kamand Campus)

School of Computing and Electrical Engineering (SCEE)

Indian Institute of Technology, (IIT), MANDI,

Himachal Pradesh, 175005, India

E-mail: palit.sharma@gmail.com

For Further Information: *Please contact the following people:*

- i) **Dr. Satinder Kumar Sharma**, (PI), SCEE, IIT-Mandi. (*For job profile, responsibilities*) E.mail: satinder@iitmandi.ac.in
- ii) **Dr. Hitesh Shrimali**, (Co-PI) SCEE, IIT-Mandi. (*For any analog design & electronics enquiry*) E.mail: hitesh@iitmandi.ac.in
- iii). **Dr. Rahul Shrestha**, (Co-PI) SCEE, IIT-Mandi. (*For any digital design & electronics enquiry*)E.mail: rahul_shrestha@iitmandi.ac.in

Terms and Conditions:

- No TA/DA shall be paid to candidates for attending the Interview.
- The PI & Co-PI reserve the right to conduct or not conduct interview for the post, in case the circumstances may constitute a Screening Committee to fix criteria after taking into account the qualification and experience of the applicants to shortlist candidates to be called for interview.
- PI & Co-PIs reserve the right to fill up the post, not to fill up the post or cancel the advertisement in whole or partly without assigning any reason. The PI & Co-PIs will also reserve the right to place a reasonable limit on the total number of candidates as per requirement of the nature of project job to be called for interview without any further clarifications/justifications at any level. The decision of the project PIs in this regard will be final.